



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

2818 11/Election
P. Walter
3-12-03

Applicants: Jack H. Yuan et al.
Assignee: SanDisk Corporation
Title: Scalable Self-Aligned Dual floating Gate Memory Cell Array And Methods Of Forming The Array
Application No.: 09/925,102 Filing Date: August 8, 2001
Examiner: Dang, Phuc T. Group Art Unit: 2818
Docket No.: M-11822 US Conf. No.: 3186

San Francisco, California
March 4, 2003

Box Non-Fee Amendment
Commissioner for Patents
Washington, D.C. 20231

RESPONSE TO REQUIREMENT FOR RESTRICTION

Dear Sir:

In response to the Office Action dated February 5, 2003, claims 1-22 of Group I are hereby elected for examination in the present application.

EXPRESS MAIL LABEL NO:

EU972290222US

Respectfully submitted,

Gerald P. Parsons
Reg. No. 24,486

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TECHNOLOGY CENTER 2800

PARSONS HSUE &
DE RUNTZ LLP
25 Metro Drive, Suite 700
San Jose, CA 95110
(408) 453-9200
(408) 453-7979 fax